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Thu, 20 Sep 2018 23:38:00 GMT - SIT Nemesis By Nelson Pass Introduction In the Volume 0 of Linear Audio I presented a redux of the 1985 Hiraga design, the Nemesis. The article explored some of the issues and variations that

SIT Nemesis - FIRST WATT -

Sat, 15 Sep 2018 19:04:00 GMT - Introduction to Static Induction Transistors In the mid 1970's a special variety of Jfet invented in Japan called a Static Induction Transistor (SIT) found its way into the "Vfet"